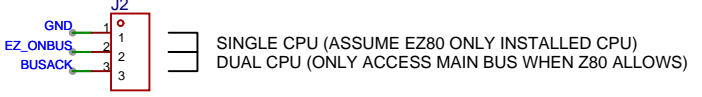


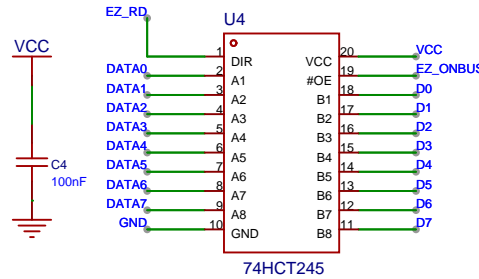
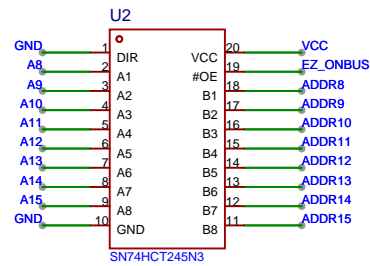
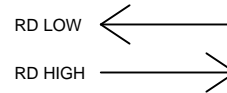
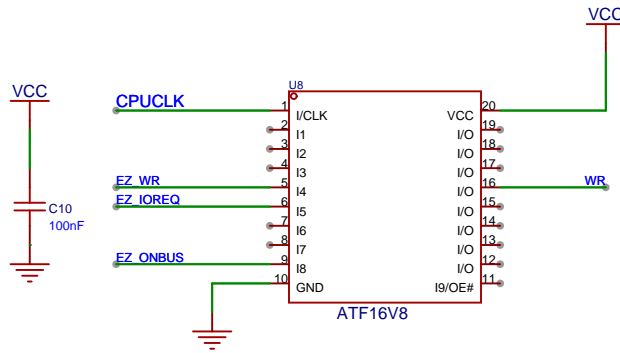
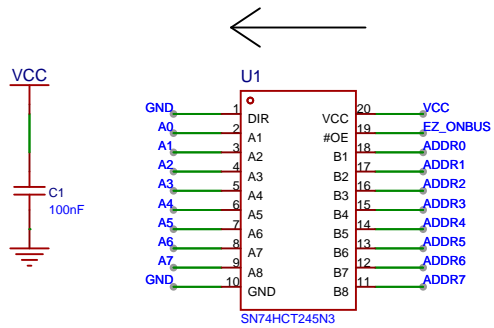
* WHEN EZ_ONBUS IS LOW, EZ80 WILL DRIVE THE ADDRESS/CONTROL BUS
 * WHEN EZ_ONBUS IS HIGH, EZ80 IS ISOLATED AND CAN ONLY ACCESS ITS INTERNAL ROM/RAM/IO



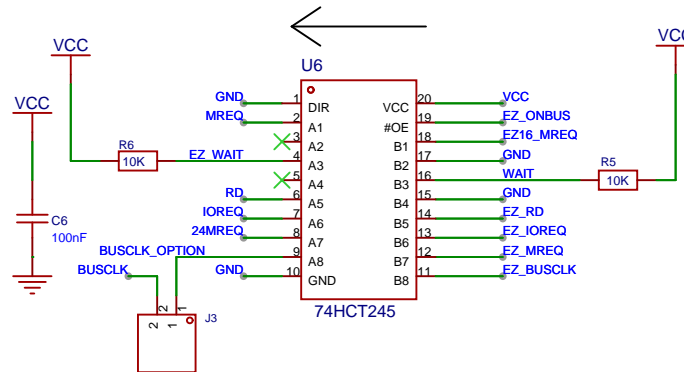
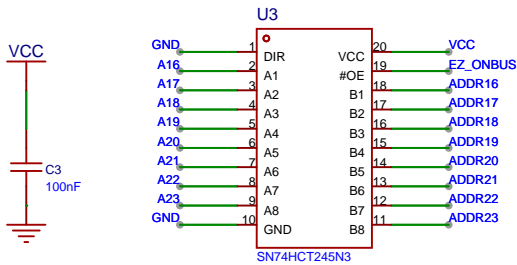
TODOS:
 1. Reduce drill size for pcb pins?
 2. Map INSTRD to M1

1. Use Round Machine Sockets for CPU's socket
2. To take pins to be diameter between 15mil (0.38mm) and 25mil (0.635mm)

EZ80-INTERFACE-V2									
	1 MAIN								
	<table border="1"> <tr> <td>Created:</td> <td>2024-05-13</td> </tr> <tr> <td>Updated:</td> <td>2024-07-15</td> </tr> <tr> <td>Version:</td> <td>V1.5</td> </tr> <tr> <td>Count:</td> <td>4</td> </tr> </table>	Created:	2024-05-13	Updated:	2024-07-15	Version:	V1.5	Count:	4
	Created:	2024-05-13							
Updated:	2024-07-15								
Version:	V1.5								
Count:	4								



1. RD/WR HIGH => BUS DATA LINE DRIVEN, EZ80 TRI
2. RD LOW => BUS DATA LINE TRI, EZ80 DATA DRIVEN
3. WR LOW => BUS DATA LINE DRIVEN, EZ80 TRI



PLD CODE

```

/***** INPUT PINS *****/
PIN 1 = CPU_CLK;
PIN 5 = EZ_WR;
PIN 6 = IORQ;
PIN 9 = EZ_ONBUS;
PIN 10 = GND;

```

```

/***** OUTPUT PINS *****/
PIN 14 = C0;
PIN 15 = C1;
PIN 16 = WR;
PIN 17 = C2;
PIN 20 = VCC;

```

field WAITCOUNT = [C0, C1, C2];

```

T00=WAITCOUNT:[b'000];
T01=WAITCOUNT:[b'001];
T02=WAITCOUNT:[b'010];
T03=WAITCOUNT:[b'011];
T04=WAITCOUNT:[b'100];
T05=WAITCOUNT:[b'101];
T06=WAITCOUNT:[b'110];
T07=WAITCOUNT:[b'111];

```

FINISHED=T03;

```

C0.D = !EZ_WR & (T00 # T02 # T04 # T06 # T07);
C1.D = !EZ_WR & (T01 # T02 # T05 # T06 # T07);
C2.D = !EZ_WR & (T03 # T04 # T05 # T06 # T07);

```

```

// IF A NON IORQ WR, THEN PASS THROUGH WR
// OTHERWISE, PASSTHROUGH ONLY FOR NEXT 5 CLOCK TICKS

```

```

WR = !(IORQ & !EZ_WR)
      # (!IORQ & !EZ_WR & (T00 # T01 # T02 # T03 # T04));

```

WR.OE = !EZ_ONBUS;

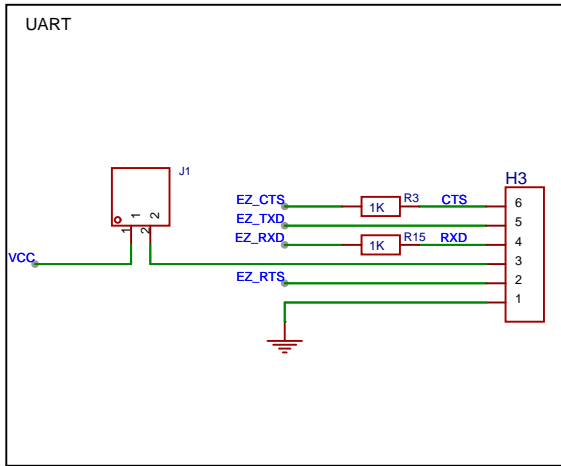
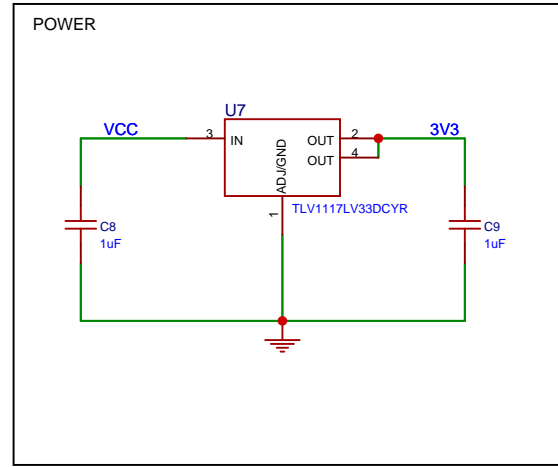
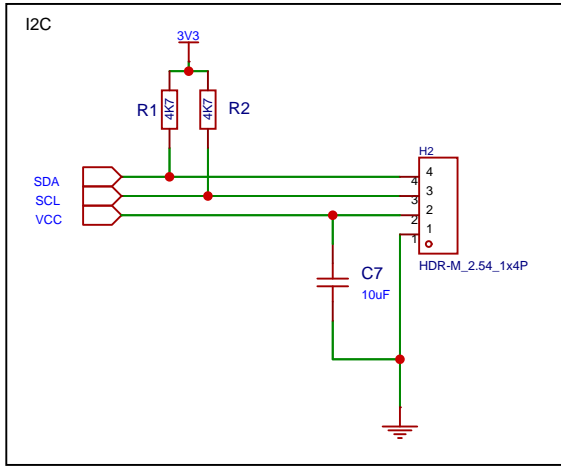
EZ80-INTERFACE-V2

Dino Boards



INTERFACE

Created:	2024-05-14
Updated:	2024-07-21
Version:	V1.5
Count:	4



EZ80-INTERFACE-V2

Dino Boards



3 ON-BOARD

Created:	2024-05-14
Updated:	2024-07-15
Version:	V1.5
Count:	4